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1	Integrated circuit cells	716/8
2	Integrated circuit cells	257/500
3	Integrated circuit cells	257/500
4	STRUCTURE AND METHOD OF FABRICATING EMBEDDED DRAM HAVING A VERTICAL DEVICE ARRAY AND A BORDERED BITLINE CONTACT	257/301
5	Semiconductor device and manufacturing method thereof	257/296
6	Vertically integrated flash EEPROM for greater density and lower cost	257/316
7	Hunting arrow rest with loading gate	124/44.5
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9	Structure and method of fabricating embedded DRAM having a vertical device array and a bordered bitline contact	257/301
10	Semiconductor device having a plurality of capacitors aligned at regular intervals	257/303
11	Chute closure apparatus	222/556